

Easy

Assistant Engineer (Electrical), Class 2025

Building Department

A 4-bit ripple counter consists of flip-flops, where each have a propagation delay from clock to Q output of 15 ns. For the counter to recycle from 1111 to 0000, it

A

15 ns

B

30 ns

C

45 ns

D

60 ns

Correct

Correct Answer: D

Full Detailed Explanation

Open the live question URL below to read the full explanation, related links and practice flow.

Open live question on website:

<https://sarkariresultout.com/pyq/hi/question/gpsc-ae-electrical-q159-ripple-counter-propagation-delay/>