

Moderate

Assistant Engineer (Electrical), Class 2025

Building Department

The group of bits 10110111 is serially shifted (rightmost bit first) into an 8-bit parallel output shift register with an initial state 11110000. After two clock pulses,

A

10111000

B

10110111

C

11110000

D

11111100

Correct

Correct Answer: D

Full Detailed Explanation

Open the live question URL below to read the full explanation, related links and practice flow.

Open live question on website:

<https://sarkariresultout.com/pyq/gu/question/gpsc-ae-electrical-q161-serial-in-parallel-out-register/>