

Easy

Assistant Engineer (Electrical), Class 2025

Building Department

When can one logic gate drive many other logic gates in Digital Electronics?

A

When its output impedance is low and the input impedance is low

B

When its output impedance is high and the input impedance is high

C

When its output impedance is high and the input impedance is low

D

When its output impedance is low and the input impedance is high **Correct**

Correct Answer: D

Full Detailed Explanation

Open the live question URL below to read the full explanation, related links and practice flow.

Open live question on website:

<https://sarkariresultout.com/pyq/gu/question/gpsc-ae-electrical-q152-logic-gate-fan-out/>