

Easy

Deputy Executive Engineer (Electricity) 2026

Ports Authority of Gujarat / Assistant Engineer (Electrical)

Consider the following statements regarding Flip Flops: 1. SR flip-flop has invalid state when both inputs are high causing undefined output. 2. JK flip-flop resolves

A

All of the above

B

2 and 3 only

C

1 and 3 only

D

1 and 2 only

Correct

Correct Answer: D

Full Detailed Explanation

Open the live question URL below to read the full explanation, related links and practice flow.

Open live question on website:

<https://sarkariresultout.com/pyq/gu/question/sr-jk-and-d-flip-flops/>