

Easy

Assistant Engineer (Electrical), Class 2025

Building Department

The pin that clears the command, request and temporary registers and internal first/last flipflop when it is set is

A

CLEAR

B

SET

C

HLDA

D

RESET

Correct

## Correct Answer: D

### Full Detailed Explanation

Concept & Reasoning DMA controllers contain internal state that must return to a known condition at system initialization. RESET disables/clears operational state so channels and mode registers can be reprogrammed safely. HOLD/HLDA are bus arbitration signals and do not perform this internal register clearing. How to Solve It in the Exam Clear internal controller state RESET. Important Exam Point Differentiate reset/init signals from bus request/acknowledge signals. Related Revision Path This question sits in the revision path Microprocessors and Interfacing DMA Controller 8237 DMA .

Open live question on website:

<https://sarkariresultout.com/pyq/question/gpsc-ae-electrical-q221-reset-input/>