

Hard

Assistant Engineer (Electrical), Class 2025

Building Department

In the case of two-transistor model (T1 & T2) of GTO with anode short, the anode short is placed between the

A

emitter of T1 and T2

B

emitter of T1 and base of T2

C

emitter of T1 and base of T1

Correct

D

emitter of T1 and collector of T2

Correct Answer: C

Full Detailed Explanation

Concept & Reasoning A PNP thyristor is represented by coupled PNP and NPN transistors. GTO design modifies this regenerative pair to permit gate-assisted turn-off. Anode shorts reduce the effective gain of one transistor path and help stored charge removal, which improves turn-off capability at the cost of some forward characteristics. How to Solve It in the Exam Relate device construction to the coupled-transistor model, not to an external load connection. Important Exam Point Anode shorts are a structural turn-off aid in the GTO. Related Revision Path This question sits in the revision path Power Electronics Thyristor Devices GTO Thyristor .

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