

Easy

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Gujarat Public Service Commission / Assistant Engineer (Electrical)

Consider the following statements regarding 8051 interfacing to peripherals:

1. 8051 supports multiple interrupt sources. 2. Interrupts can be enabled/disabled.

A**All of the above****Correct****B**

2 and 3 only

C

1 and 3 only

D

1 and 2 only

Correct Answer: A

Full Detailed Explanation

Correct Answer: A - All of the above

Quick Concept Explanation The classic 8051 is an 8-bit Harvard-style microcontroller with separate code and data spaces. The standard core has 4 KB on-chip program ROM and 128 bytes internal RAM, supports external memory, multiple interrupts and configurable interrupt priority. Key relation: Reset SP=07H; classic internal RAM=128 bytes; standard ROM=4 KB. Exam focus: Remember 07H for stack pointer reset. Common trap: 00H is not the reset value of the 8051 stack pointer.

Statement-wise Verification

Statement 1 - Correct. 8051 supports multiple interrupt sources. The standard core has 4 KB on-chip program ROM and 128 bytes internal RAM, supports external memory, multiple interrupts and configurable interrupt priority.

Statement 2 - Correct. Interrupts can be enabled/disabled. The standard core has 4 KB on-chip program ROM and 128 bytes internal RAM, supports external memory, multiple interrupts and configurable interrupt priority.

Statement 3 - Correct. Interrupt priority can be configured. The standard core has 4 KB on-chip program ROM and 128 bytes internal RAM, supports external memory, multiple interrupts and configurable interrupt priority.

Core Concept The classic 8051 is an 8-bit Harvard-style microcontroller with separate code and data spaces. The standard core has 4 KB on-chip program ROM and 128 bytes internal RAM, supports external memory, multiple interrupts and configurable interrupt priority.